

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	24737983	@ad<"20031014"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 14:57
L2	158	(Carlos near2 Gonzalez).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L3	53	(Kevin near2 Conley).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L4	20	L2 and L3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L5	72162	"365"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L6	29217	"711"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L7	101025	Flash adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L8	429326	EEPROM or EPROM or ROM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L9	2602	wear near2 level\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00

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L10	3206	address near2 boundar\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L11	3206	address near2 boundar\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L12	28470	address near2 assign\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L13	1903	address near2 zone\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L14	493	map\$4 near (logical adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L15	2	(address near2 boundar\$4) same reassign\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L16	101025	Flash adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L17	429326	EEPROM or EPROM or ROM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L18	466975	L16 or L17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L19	2602	wear near2 level\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00

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L20	466975	L16 or L17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L21	142	L19 and L20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L22	3206	address near2 boundar\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L23	142	L19 and L20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L24	1	L22 and L23	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L25	493	map\$4 near (logical adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L26	3	L23 and L25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L27	72162	"365"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L28	72162	"365"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L29	29217	"711"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00

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L30	99129	L28 or L29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L31	99129	L28 or L29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:00
L32	74	L31 and L23	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:01
L33	42	L1 and L32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:01
L34	0	4 and 33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/26 15:01

Terms used **EEPROM flash wear boundary**

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- 1 [Embedded systems: applications, solutions and techniques \(EMBS\): A fast start-up technique for flash memory based computing systems](#)



Keun Soo Yim, Jihong Kim, Kern Koh

March 2005 **Proceedings of the 2005 ACM symposium on Applied computing SAC '05**

Publisher: ACM Press

Full text available:  pdf(324.29 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Flash memory based embedded computing systems are becoming increasingly prevalent. These systems typically have to provide an instant start-up time. However, we observe that mounting a file system for flash memory takes 1 to 25 seconds mainly depending on the flash capacity. Since the flash chip capacity is doubled in every year, this mounting time will soon become the most dominant reason of the delay of system start-up time. Therefore, in this paper, we present instant mounting techniques for ...

Keywords: fast booting, fast mounting, flash memory, metadata snapshot

- 2 [System architecture directions for networked sensors](#)



Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister

November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Architecture News , Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX**, Volume 34 , 28 Issue 5 , 5

Publisher: ACM Press

Full text available:

Full text available:  [pdf\(1.32 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citings](#), [index terms](#)

Technological progress in integrated, low-power, CMOS communication devices and sensors makes a rich design space of networked sensors viable. They can be deeply embedded in the physical world and spread throughout our environment like smart dust. The missing elements are an overall system architecture and a methodology for systematic advance. To this end, we identify key requirements, develop a small device that is representative of the class, design a tiny event-driven operating system, and sh ...

4 [A kernel-based learning approach to ad hoc sensor network localization](#)



Xuanlong Nguyen, Michael I. Jordan, Bruno Sinopoli

August 2005 **ACM Transactions on Sensor Networks (TOSN)**, Volume 1 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(743.41 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We show that the coarse-grained and fine-grained localization problems for ad hoc sensor networks can be posed and solved as a pattern recognition problem using kernel methods from statistical learning theory. This stems from an observation that the kernel function, which is a similarity measure critical to the effectiveness of a kernel-based learning algorithm, can be naturally defined in terms of the matrix of signal strengths received by the sensors. Thus we work in the natural coordinate sys ...

Keywords: Ad hoc wireless sensor networks, kernel methods, localization, position estimation, statistical machine learning

Results 1 - 4 of 4

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| #10 | ((flash memory)<in>metadata) <OR> ((EEPROM or EPROM or PROM <IN>metadata)) <AND> ((boundary or bound or zone or border or divider<IN>metadata)) <AND> ((wear level<IN>metadata)) |
| #11 | ((flash memory)<in>metadata) <OR> ((EEPROM or EPROM or PROM <IN>metadata)) <AND> ((boundary or bound or zone or border or divider<IN>metadata)) <AND> ((wear level<IN>metadata)) <AND> ((address assignment) or (address reassignment)<IN>metadata) |
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